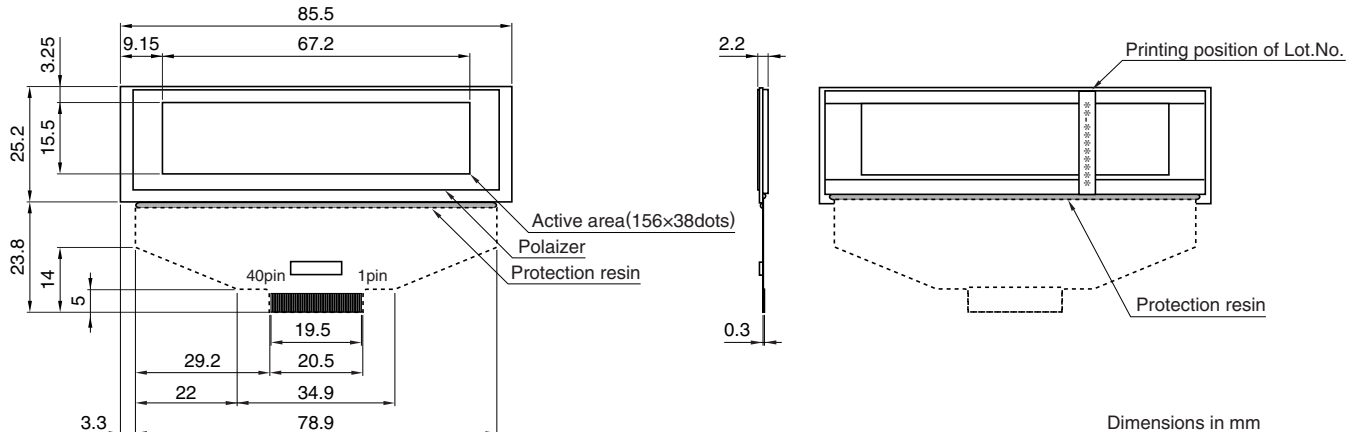⌂**TDK**

Organic Light Emitting Display
Mono-color Type/Passive Matrix Method

Conformity to RoHS Directive

UEL Series UEL127

SHAPES AND DIMENSIONS

Active area(156×38dots)
Polaizer
Protection resin

40pin 1pin

85.5
9.15 67.2
3.25
25.2
15.5
23.8
14
5
19.5
29.2
20.5
22
34.9
3.3
78.9

Printing position of Lot.No.

2.2

0.3

Protection resin

Dimensions in mm

SPECIFCATIONS

Model	UEL127
Resolution(W×H)	156×38
Active area(mm)	67.2×15.5
Pixel pitch(mm) (Dot pitch)	0.43×0.41
Thickness(mm)	2.2typ.
Weight(g)	10
Luminance(cd/m^2)	120
Circular polarizer(CPL)	With CPL
Contrast	120:1
Color/Gray scale	1(white)/4
Half lifetime of luminance(Reference value) [Hours, 25°C, lighting rate50%]	≧ 30,000
Operating temperature range(°C)	–20 to +85
Storage temperature range(°C)	–40 to +105
Connection	COF
Interface	Parallel
Panel drive voltage(V)	16.0typ.
Logic power voltage(V)	VDD: 3.0typ.
Power current(mA)	57
Power consumption(mW)	950max.

PACKAGING STYLE AND QUANTITIES
- To be separately arranged.

PRECAUTIONS
- The performance of this product is defined under particular conditions, and cannot be guaranteed under conditions of actual use.
- This product is not to be applied for uses in which the absolute highest reliability is required due to the possibility of serious human injury or physical damage. For those cases, please contact us in advance.
- If Foreign Exchange and Foreign Trade Law are applicable to this product (including service) requiring it to be regulated, export admission is needed under law.
- The information in this document is provided as an example only, and cannot be guaranteed as the final design for mass production.
- Please contact us for detailed information on the shape of the FPC part.

- Under license of KODAK OLED patents.
- Conformity to RoHS Directive: This means that, in conformity with EU Directive 2002/95/EC, lead, cadmium, mercury, hexavalent chromium, and specific bromine-based flame retardants, PBB and PBDE, have not been used, except for exempted applications.

PIN ASSIGNMENT

Pin No.	Pin name	Function description	I/O
1	VSSR	Cathode GND	P
2	VSSC	Anode GND	P
3	VDISPC	Anode power supply	P
4	VDISPR	Cathode power supply	P
5	VSSCORE	Logic GND	P
6	C80	Interface select	I
7	VDDCORE	Logic power supply	P
8	A0	Data/command select	I
9	CSB	Chip select	I
10	RDB	Read	I
11	WRB	Write	I
12	RSTB	Reset	I
13	VDDIO	Interface power supply	P
14	D15	Data signal	I/O
15	D14	Data signal	I/O
16	D13	Data signal	I/O
17	D12	Data signal	I/O
18	D11	Data signal	I/O
19	D10	Data signal	I/O
20	D9	Data signal	I/O
21	D8	Data signal	I/O
22	D7	Data signal	I/O
23	D6	Data signal	I/O
24	D5	Data signal	I/O
25	D4	Data signal	I/O
26	D3	Data signal	I/O
27	D2	Data signal	I/O
28	D1	Data signal	I/O
29	D0	Data signal	I/O
30	N.C.	No connection	—
31	OSCAIO	Adjust of oscillation frequency	P
32	N.C.	No connection	—
33	VDDIO	Interface power supply	P
34	VDDCORE	Logic power supply	P
35	VSSC	Anode GND	P
36	VDISPC	Anode power supply	P
37	VSYNC	Vertical synchronizing signal	O
38	REL	Segment current reference pin	P
39	VDDCORE	Logic power supply	P
40	VREGEXT	Generation of internal power supply	P

- All specifications are subject to change without notice.